ACKNOWLEDGEMENT RECEIPT

Electronic Version

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Title of Invention

METHOD AND CIRCUIT FOR PRECISE TIMING OF SIGNALS IN AN EMBEDDED DRAM ARRAY

Submision Type: **Utility Patent Filing**

Application Number:

Server Response:

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42652 EFS ID:

Confirmation Code	Message
ISVR1	Submission was successfully submitted - Even if Informational or Warning Messages appear below, please do not resubmit this application
ICON1	1183
WGEN24	Filename= all XMLs having <invention-title> BusinessRule= Common elements across multiple XML documents within the submission package do not have synchronized data content. #Supporting Msg:<invention-title> not the same or having font fomat.</invention-title></invention-title>

Darren Anand First Named Applicant:

Attorney Docket Number: BUR920020144US1

Timestamp: 2003-06-30 14:08:30 EDT

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File Listing:

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us-assignment	us-assignment.dtd	10898
us-assignment	BUR920020144assign.tif	45632
us-request	BUR920020144US1-usrequ.xml	2954
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us-fee-sheet	BUR920020144US1-usfees.xml	2041
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us-fee-sheet	us-fee-sheet.dtd	10901
application-body	BUR920020144SPEC-trans.xml	53601
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